

SW : SWITCH

U1~U4 : U-LINK

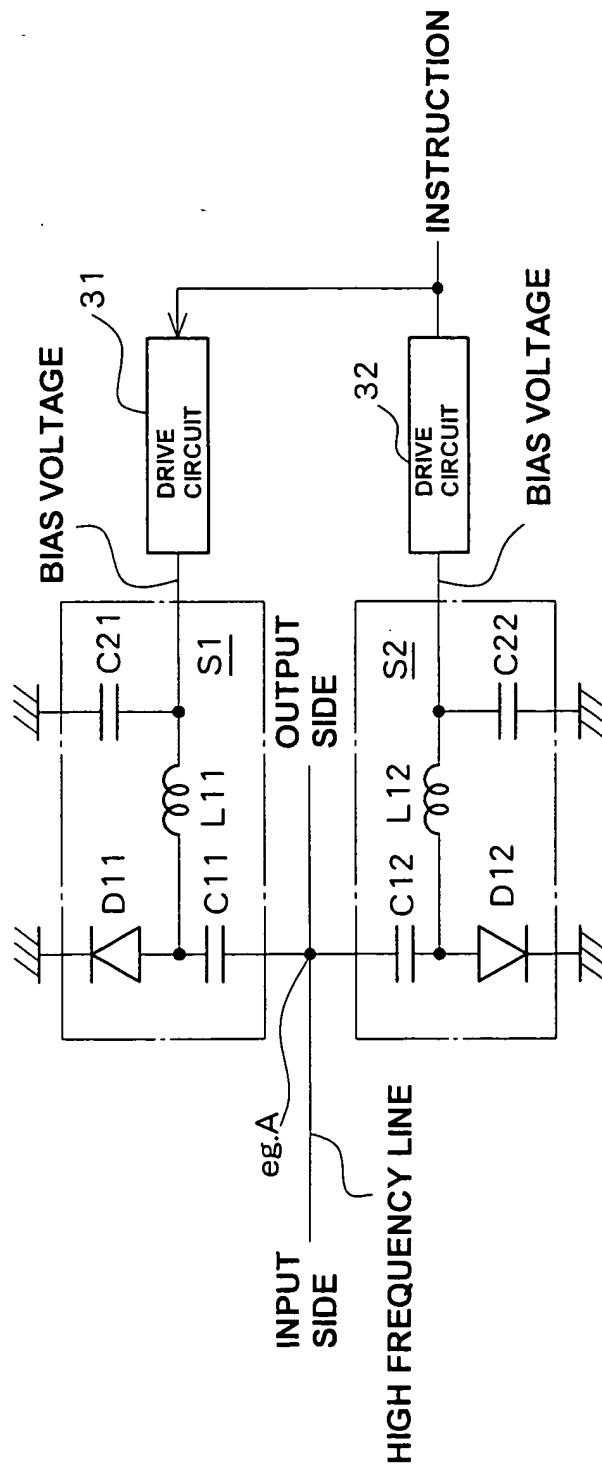
S1, S2 : SHUNT-CIRCUIT

$n=1, 2, 3 \dots$ (INTEGER)

A~G : SHUNT CIRCUIT PROVISION POSITION

a~g : U-LINK END POSITION

Fig. 1



D11, D12 : PIN DIODE

Fig. 2

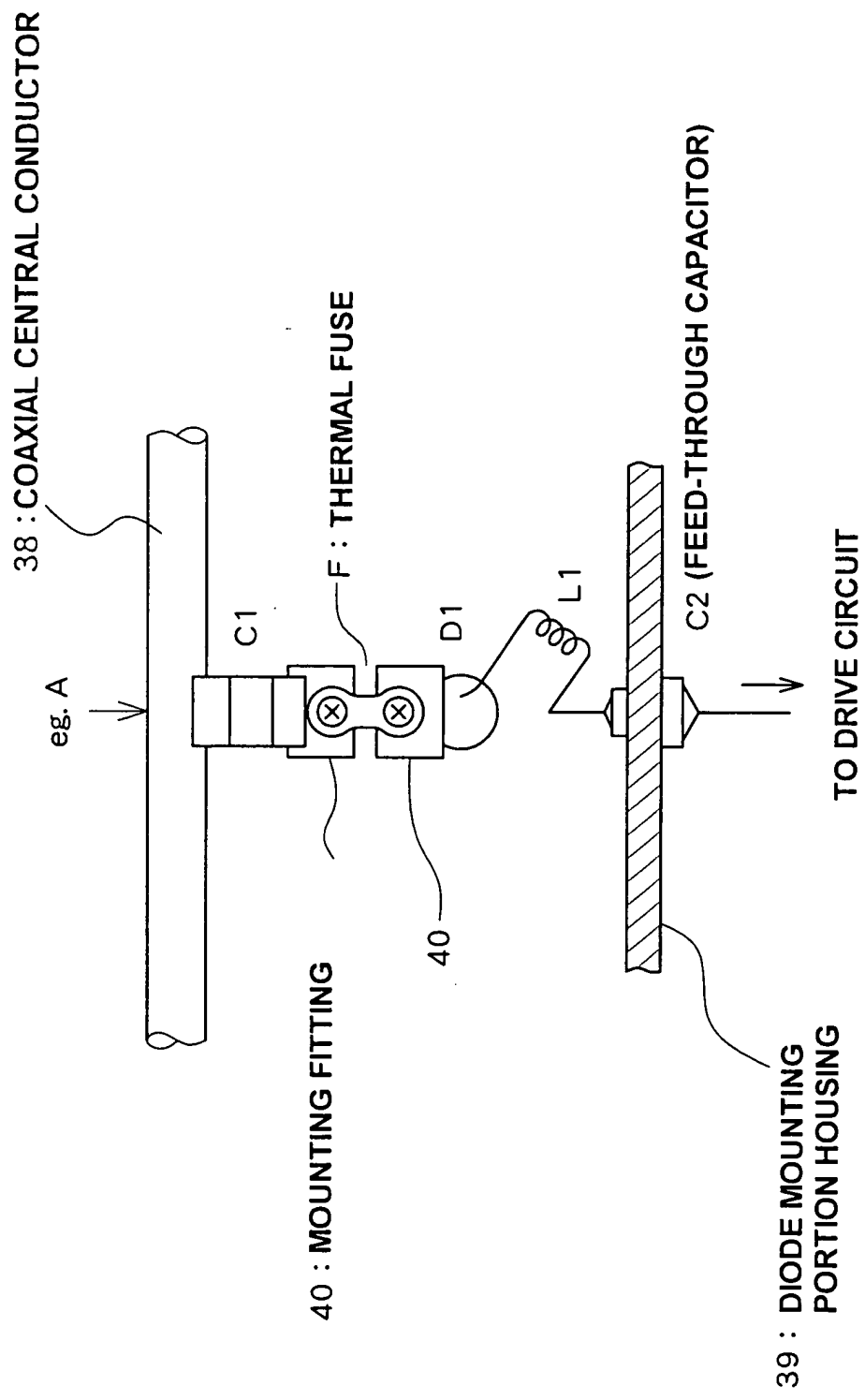
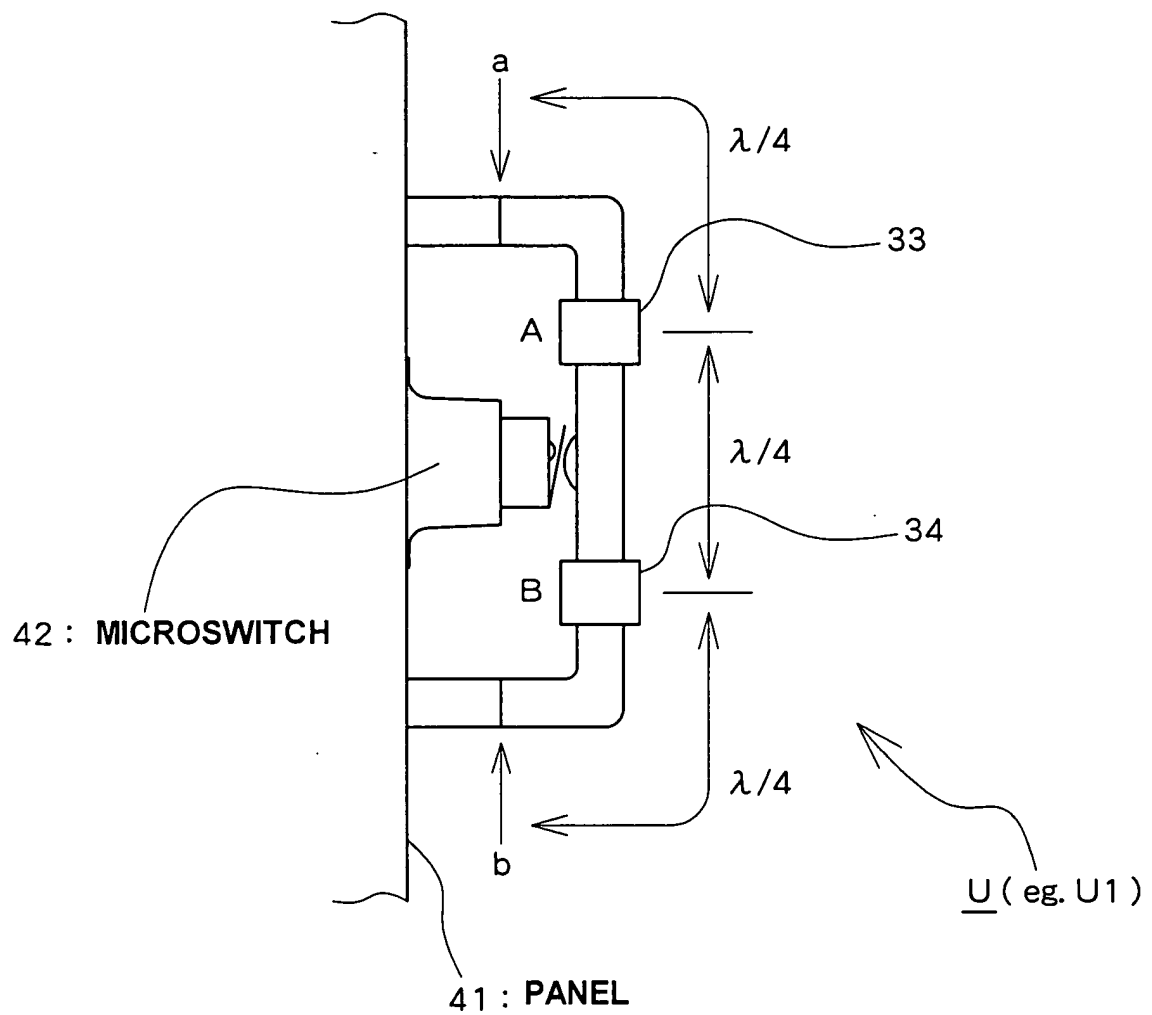


Fig. 3



33, 34 : DIODE MOUNTING PORTION

Fig. 4

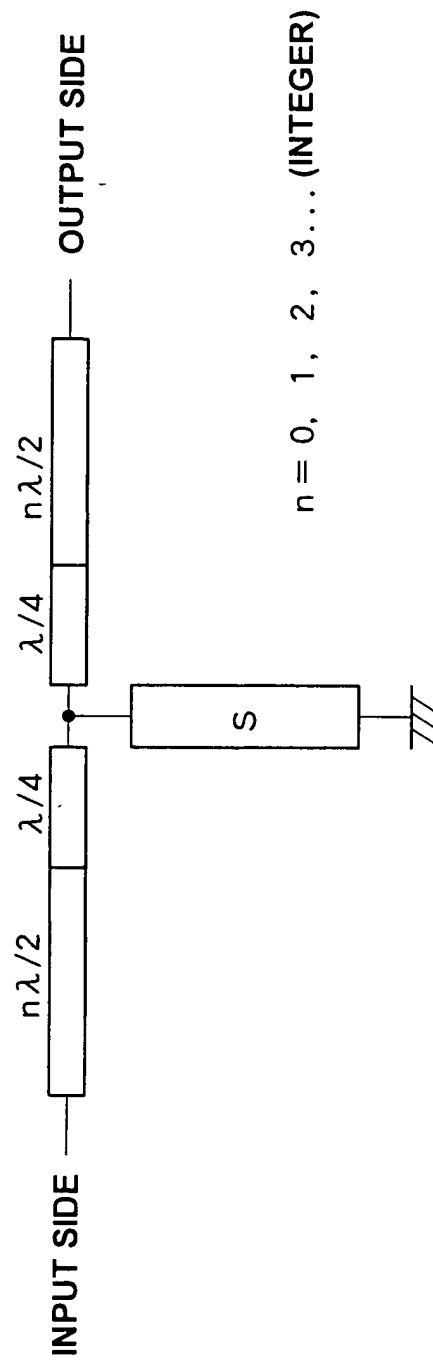
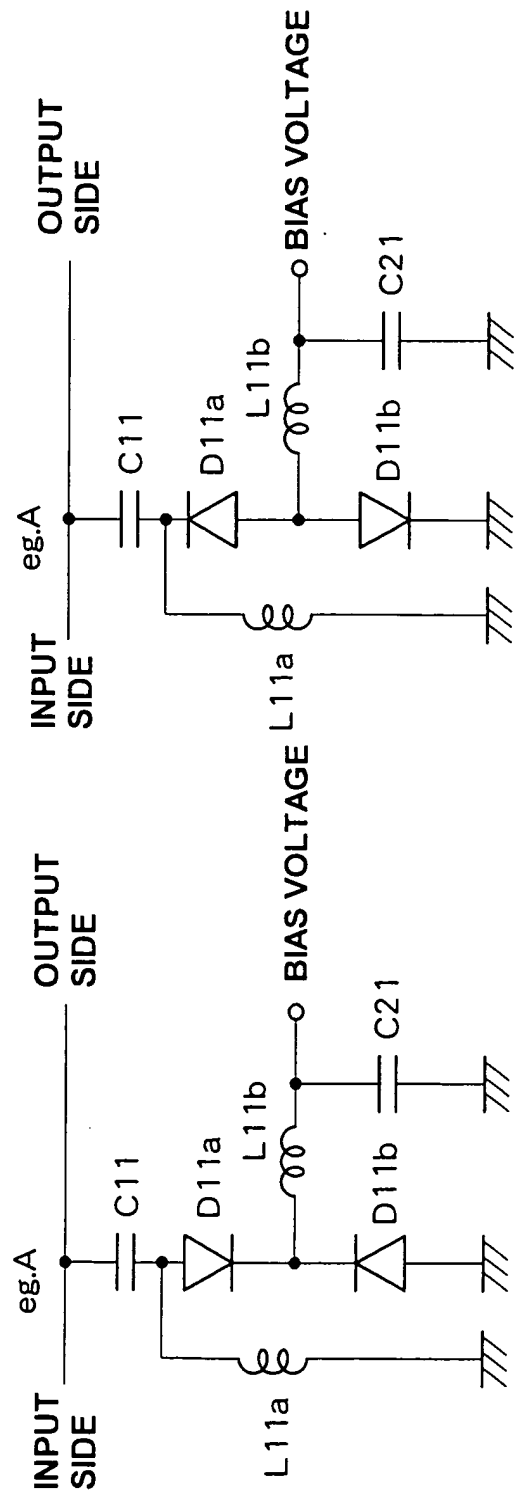


Fig. 5



(a)

(b)

Fig. 6

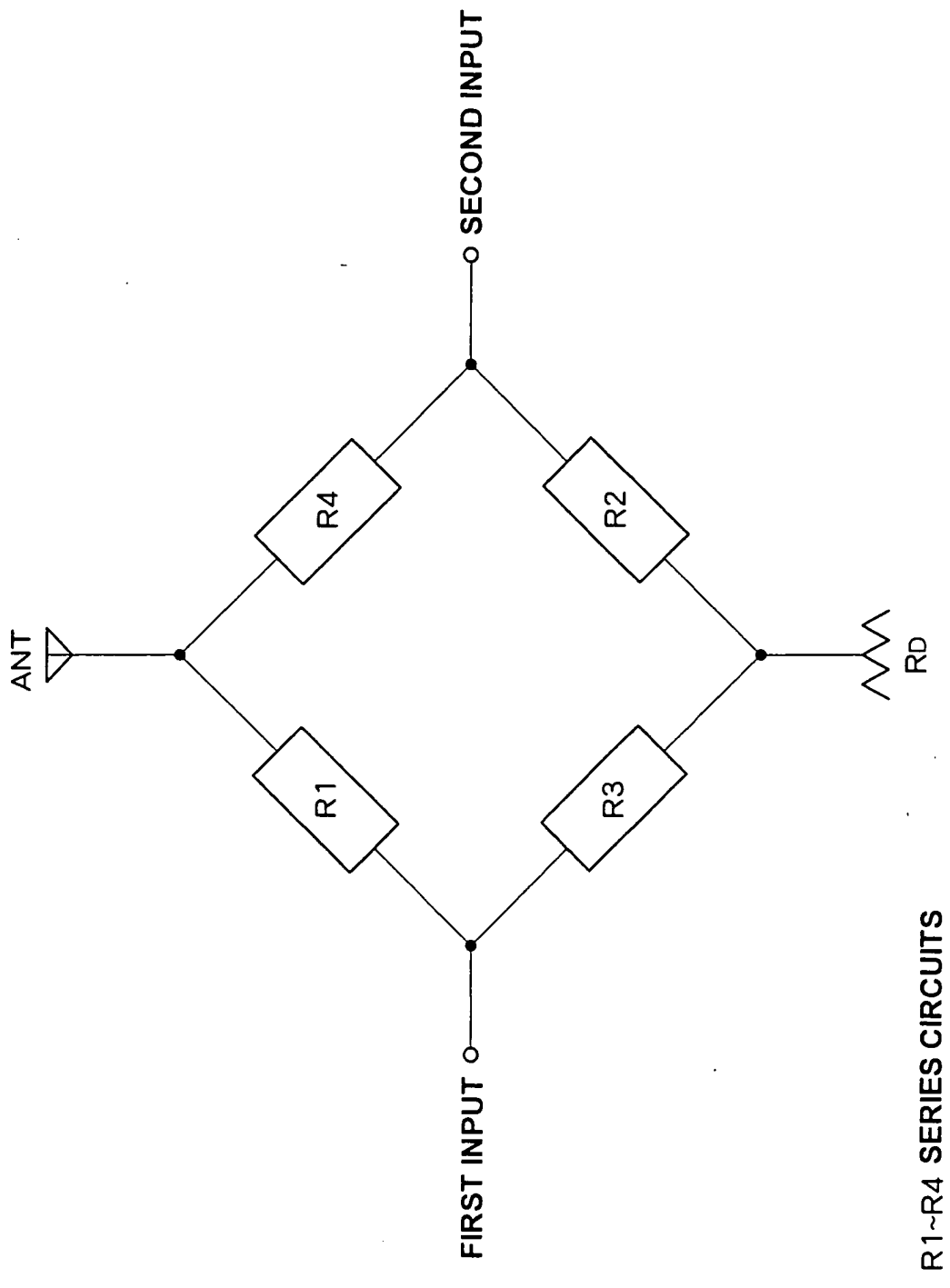


Fig. 7

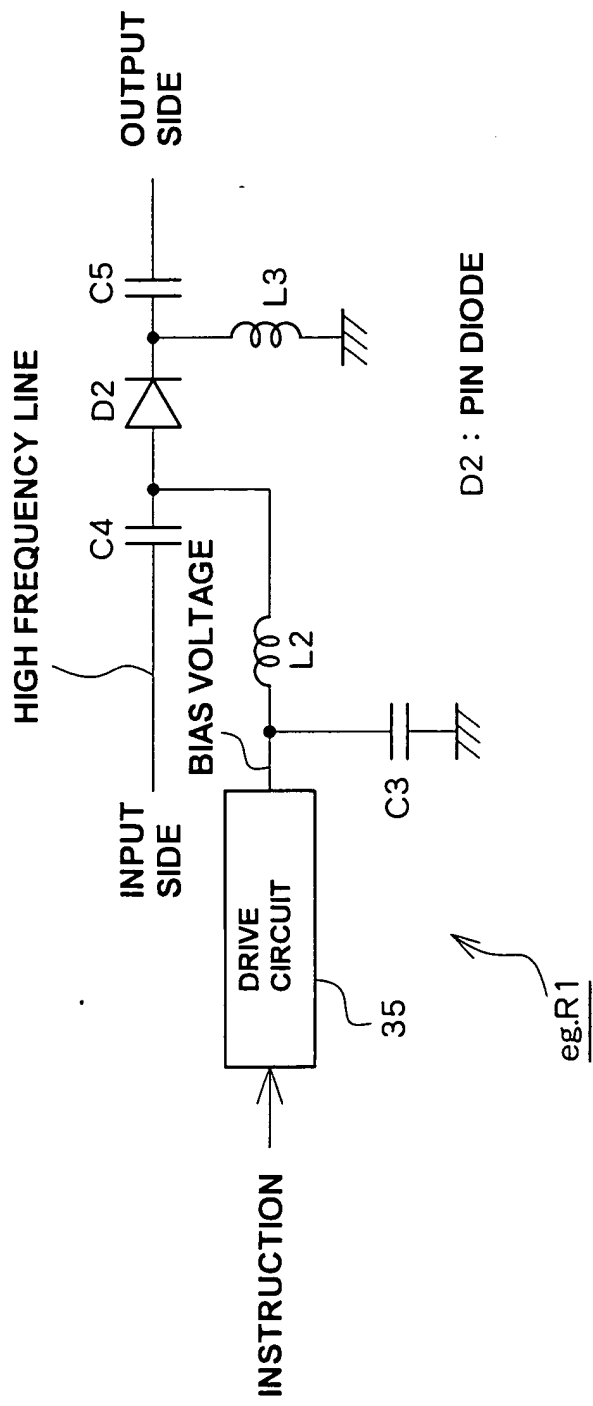


Fig. 8

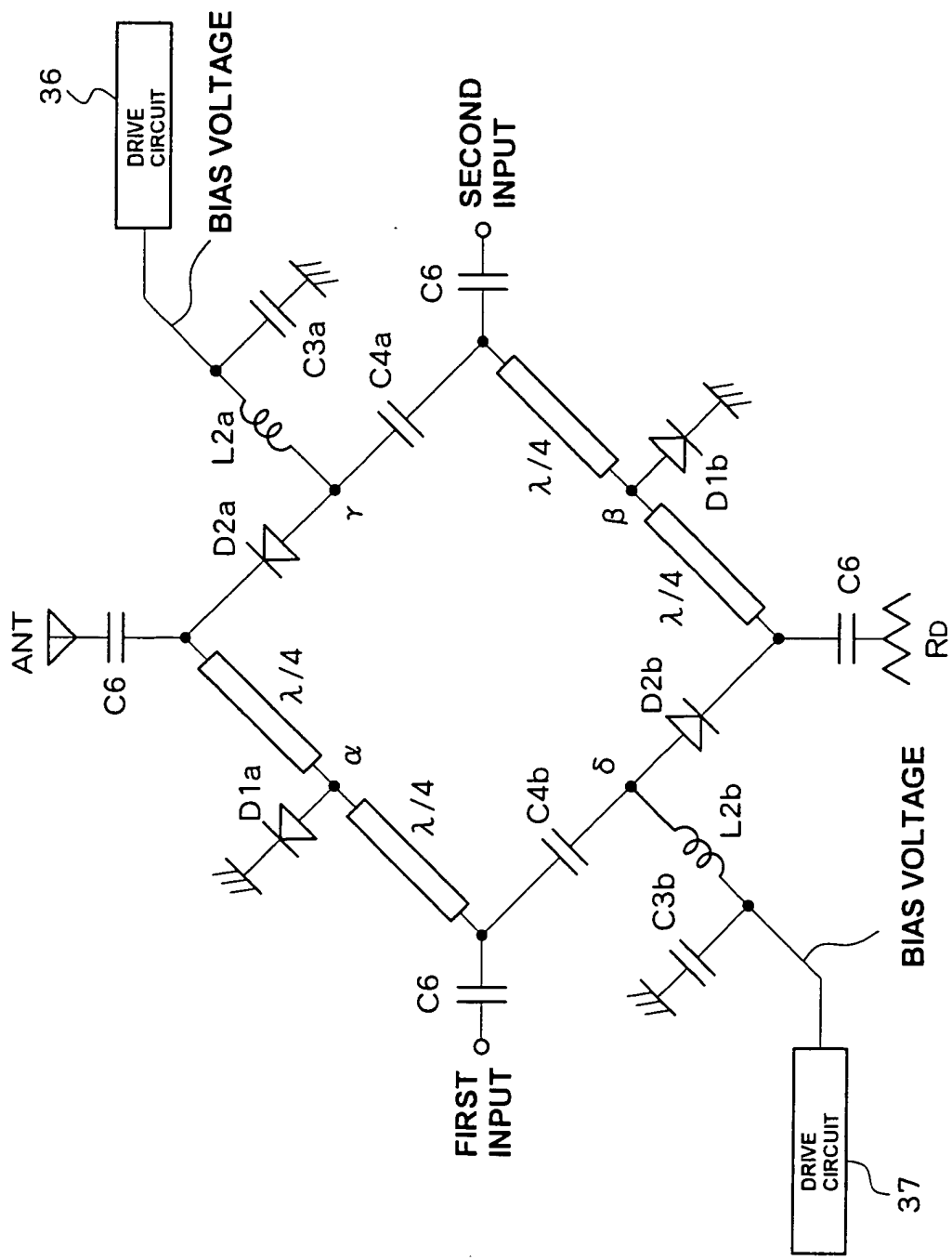


Fig. 9

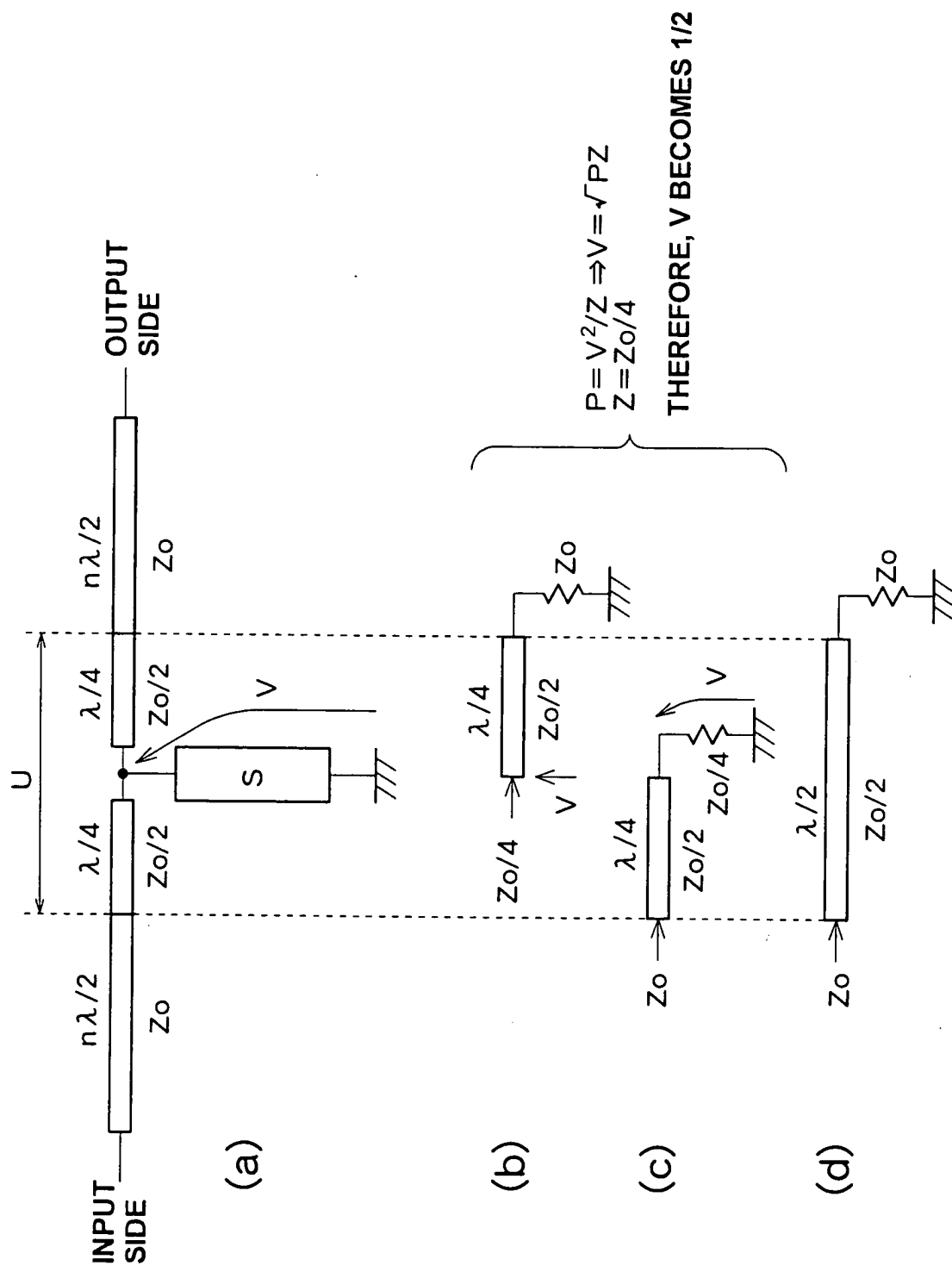


Fig. 10

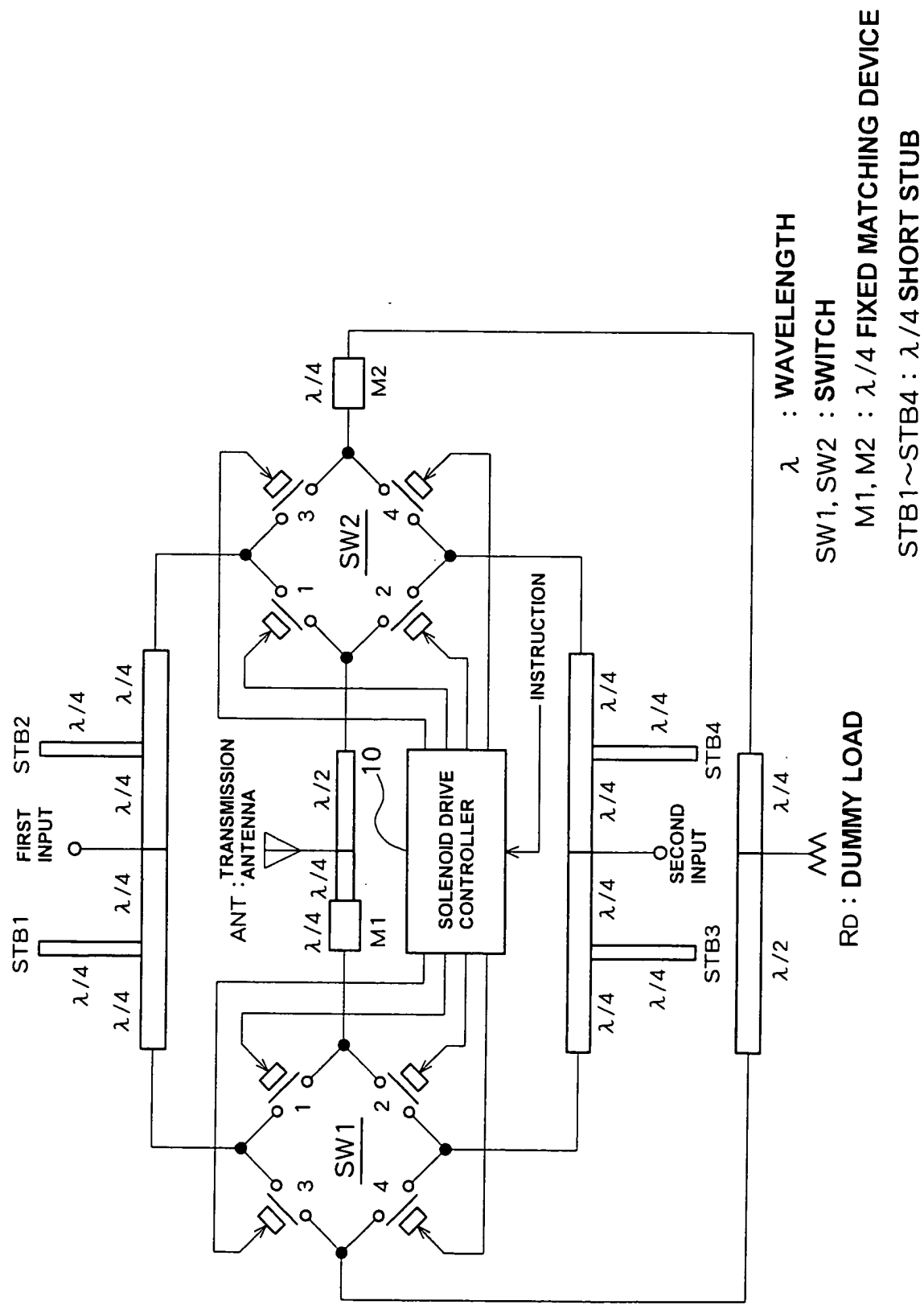


Fig. 11

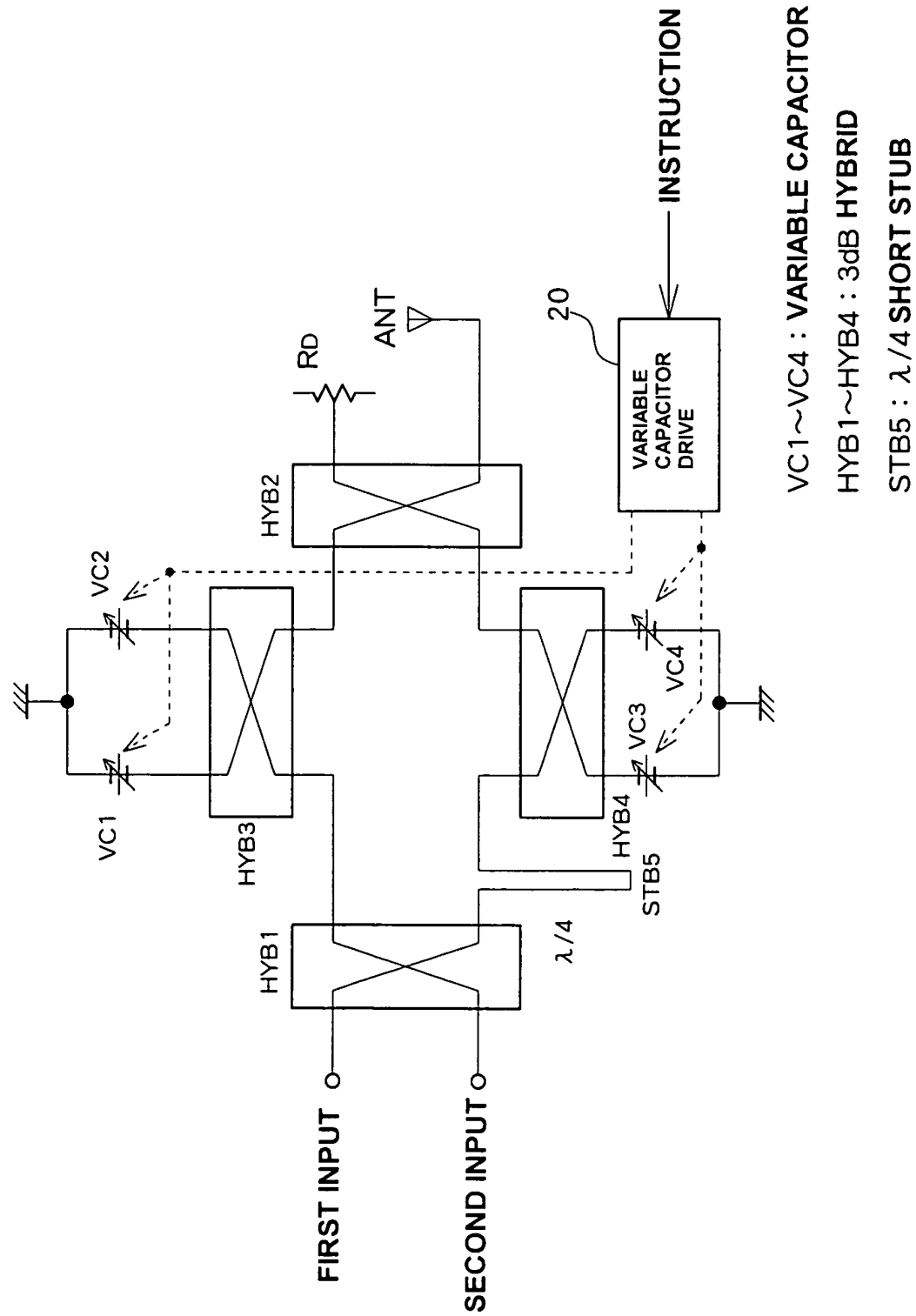
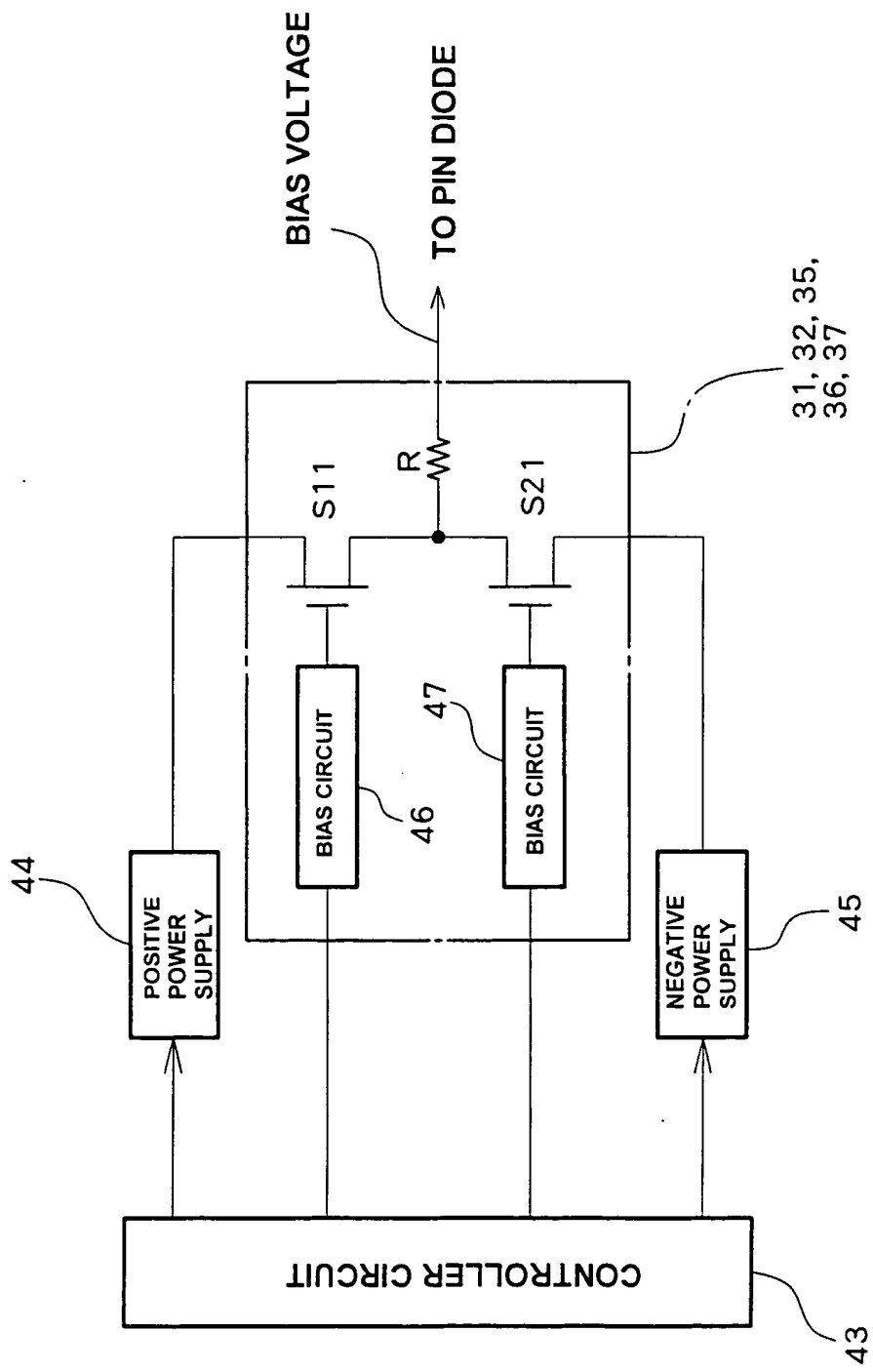


Fig. 12



S11, S21 : SEMICONDUCTOR SWITCH

Fig. 13

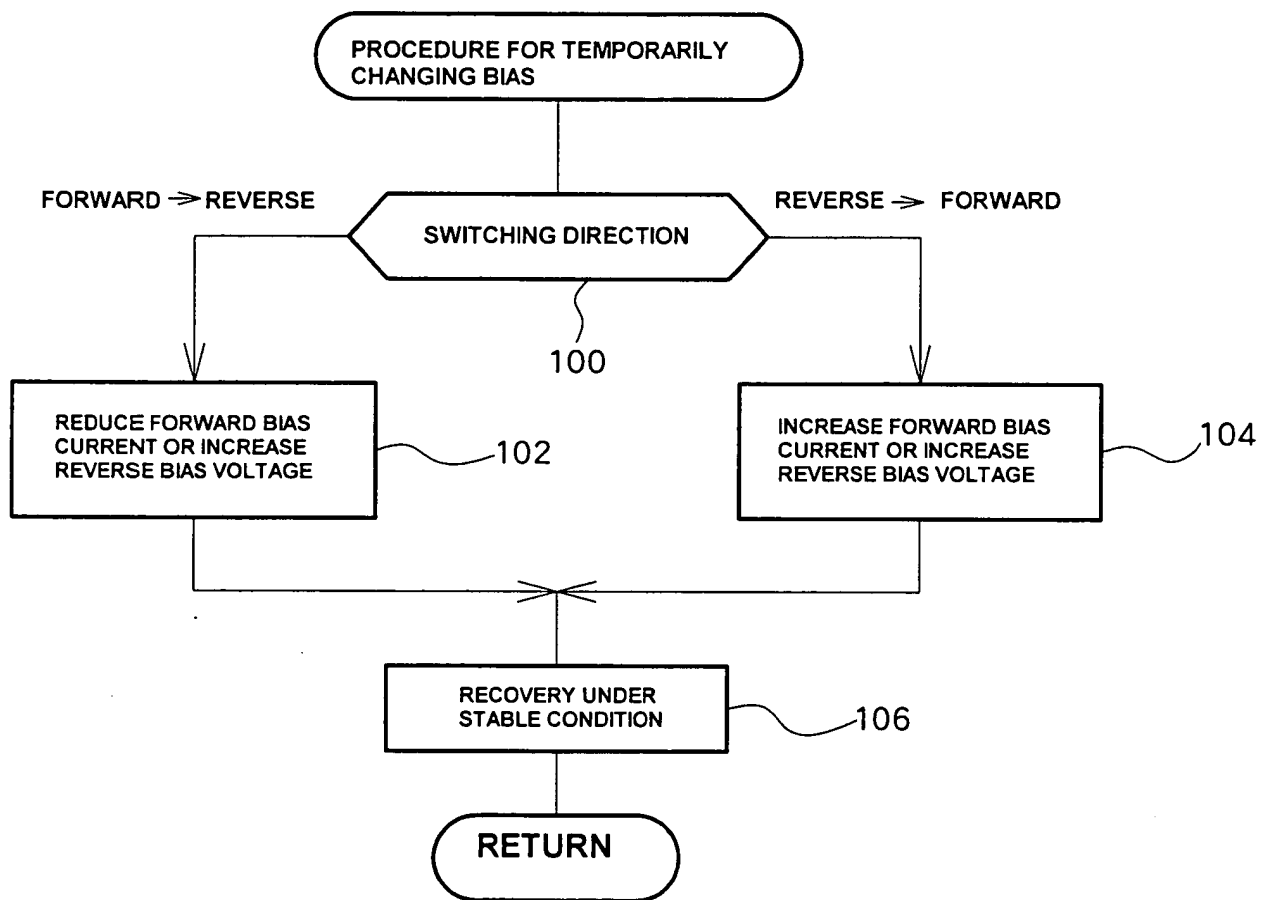


Fig. 14